

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims:

1. (Currently Amended) A data driving circuit of an LCD device comprising:

a timing controller for formatting input data so that data and gate drivers of an LCD panel display a picture image, and outputting a selection signal;

a plurality of digital to analog converters for converting digital image signals output from the timing controller to analog image signals based on a number of bits corresponding to a [[color]] gray level displayed and for receiving the selection signal; and

a plurality of amplifiers for amplifying the analog image signals output from the respective digital to analog converters and outputting the amplified image signals to the LCD panel,

wherein the selection signal selects an appropriate converter of the plurality of digital to analog converters to be driven according to the number of bits corresponding to the [[color]] gray level.

2. (Original) The data driving circuit of the LCD device as claimed in claim 1, further comprising a plurality of multiplexers for selecting a signal output from one of the digital to analog converters in accordance with the selection signal of the timing controller and outputting the selected signal to the LCD panel.

3. (Original) The data driving circuit of the LCD device as claimed in claim 1, comprising a first digital to analog converter and a second digital to analog converter, the first

digital to analog converter serving to obtain a multigray (64 gray or 6 bit) image, and the second digital to analog converter serving to obtain a low gray (2 gray, 1 bit) image.

4. (Original) The data driving circuit of the LCD device as claimed in claim 1, comprising a first digital to analog converter, a second digital to analog converter and a third digital to analog converter, the first digital to analog converter serving to obtain a multigray (64 gray or 6 bit) image, the second digital to analog converter serving to obtain an intermediate gray (16 gray or 4 bit) image, and the third digital to analog converter serving to obtain a low gray (2 gray or 1 bit) image.

5. (Currently Amended) A data driving circuit of an LCD device comprising:

a timing controller for formatting input data so that data and gate drivers of an LCD panel display a picture image, and outputting a selection signal;

a level shifter for amplifying voltage levels of signals output from the timing controller;

a plurality of digital to analog converters for converting digital image signals output from the level shifter to analog image signals based on a number of bits corresponding to a [[color]] gray level displayed and for receiving the selection signal;

a plurality of amplifiers for amplifying the analog image signals output from the respective digital to analog converters and outputting the amplified image signals to the LCD panel; and

a plurality of multiplexers for selecting a signal output from one of the plurality of amplifiers in accordance with the selection signal of the timing controller and outputting the selected signal to the LCD panel,

wherein the selection signal selects an appropriate converter of the plurality of digital to analog converters to be driven according to the [[color]] gray level.

6. (Currently Amended) The data driving circuit of the LCD device as claimed in claim 5, comprising a first digital to analog converter and a second digital to analog converter, the first digital to analog converter serving to obtain a multigray (64 gray or 6 bit) image, and the second digital to analog converter serving to obtain a low gray (2 gray, 1 bit) image[()].

7. (Original) The data driving circuit of the LCD device as claimed in claim 5, comprising a first digital to analog converter, a second digital to analog converter and a third digital to analog converter, the first digital to analog converter serving to obtain a multigray (64 gray or 6 bit) image, the second digital to analog converter serving to obtain an intermediate gray (16 gray or 4 bit) image, and the third digital to analog converter serving to obtain a low gray (2 gray or 1 bit) image.